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<b>UTILITY PATENT APPLICATION TRANSMITTAL</b>  <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>	Attorney Docket No.	M-5599-2D US
	First Named Inventor or Application Identifier	Thomas P. Glenn
	Title	Plastic Integrated Circuit Package And Method And Leadframe For Making The Package
	Express Mail Label No.	EL 579 029 877 US

<b>APPLICATION ELEMENTS</b> <small>See MPEP chapter 600 concerning utility patent application contents</small>	<b>ADDRESS TO:</b> Assistant Commissioner for Patents Box Patent Application Washington, D.C. 20231
1. <input checked="" type="checkbox"/> <b>Fee Transmittal Form - see page 2 of this form.</b> <small>(Submit an original, and a duplicate for fee processing)</small> 2. Application: <input checked="" type="checkbox"/> <b>Specification:</b> (preferred arrangement set forth below) Field of the Invention, Background of the Invention, Summary of the Invention, Brief Description of the Drawings, and Detailed Description (all totaling 38 pages)) <input checked="" type="checkbox"/> <b>Claim(s)</b> 13 pages <input checked="" type="checkbox"/> <b>Abstract of the Disclosure</b> 1 page 3. <input checked="" type="checkbox"/> <b>Drawing(s)</b> (35 USC 113) [Total Sheets 6] Figs. 1, 2, 3, 4, 5, 6, 7, 8, 9, 10 and 11 4. <b>Oath or Declaration</b> <input type="checkbox"/> unsigned [Total Pages ____] a. <input type="checkbox"/> Newly executed (original or copy) b. <input checked="" type="checkbox"/> Copy from prior application (37 CFR §1.63(d)) (for continuation/divisional with Box 17 completed) c. <input type="checkbox"/> <b>DELETION OF INVENTOR(S)</b> Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b) 5. <input checked="" type="checkbox"/> <b>Incorporation By Reference</b> (useable if Box 4b is checked) The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.	
6. <input type="checkbox"/> <b>Microfiche Computer Program Appendix</b> consisting of ____ pages of microfiche containing ____ frames on each page in accompanying envelope. 7. <b>Nucleotide and/or Amino Acid Sequence Submission</b> (if applicable, all necessary) a. <input type="checkbox"/> Computer Readable Copy b. <input type="checkbox"/> Paper Copy (identical to computer copy) c. <input type="checkbox"/> Statement verifying identity of above copies	
<b>ACCOMPANYING APPLICATION PARTS</b> 8. <input type="checkbox"/> Assignment Papers (cover sheet & documents) ____ pages 9. <input type="checkbox"/> 37 CFR §3.73(b) Statement <input checked="" type="checkbox"/> Power of Attorney (combined when there is an ____ with Patent Declaration Assignee) above.) 10. <input type="checkbox"/> English Translation Document (if applicable) 11. <input type="checkbox"/> Information Disclosure Statement (IDS) & <input type="checkbox"/> PTO-1449 <input type="checkbox"/> ____ Copies of IDS Citations/References 12. <input checked="" type="checkbox"/> Preliminary Amendment 1 page 13. <input checked="" type="checkbox"/> Return Receipt Postcard (MPEP 503) (should be specifically itemized) 14. <b>Small Entity Status</b> <input type="checkbox"/> Small Entity Statement Enclosed ____ pages <input type="checkbox"/> Statement filed in prior application; and status still proper and desired <input type="checkbox"/> Is no longer claimed. 15. <input type="checkbox"/> Certified Copy of Priority Document(s) (if foreign priority is claimed) 16. <input type="checkbox"/> Other: <input type="checkbox"/> Copy of Petition for Extension of Time filed in parent appl.; <input type="checkbox"/>	

17. If a CONTINUING APPLICATION, check appropriate box and supply the requisite information and a preliminary amendment:  
☐ Continuation ☒ Divisional of prior application No. 09/103,760  
 Filed on June 24, 1998, entitled: Plastic Integrated Circuit Package And Method And Leadframe For Making The Package.  
 PRIOR APPLICATION INFORMATION: Examiner: R. Mancho Group Art Unit: 3661

**18. CORRESPONDENCE ADDRESS**

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**19. Fee calculations.****(After Preliminary Amendment)**

CLAIMS (Number Filed)	(1) FOR	(2)		(3) NUMBER EXTRA		(4) RATE		(5) CALCULATIONS
20	TOTAL CLAIMS (37 CFR 1.16(c))	-20	=	0	x	\$18	=	\$0.00
5	INDEPENDENT CLAIMS (37 CFR 1.16(b))	-3	=	2	x	\$78	=	\$156.00
<input type="checkbox"/>	MULTIPLE DEPENDENT CLAIMS (if applicable) (37 CFR 1.18(d))				+	\$260.00	=	
						BASIC FEE (37 CFR 1.16(a))	=	\$690.00
						Total of above Calculations	=	\$846.00
						Reduction by 50% for filing by small entity (Note 31 CFR 1.9, 1.27, 1.28).	=	
						TOTAL	=	\$846.00

20. **FEES:** The Commissioner is hereby authorized to credit overpayments or charge the following fees to Deposit Account No. **19-2386**:

- a. ☒ Fees required under 37 CFR 1.16. (U.S. Application Filing Fees)
- b. ☒ Fees required under 37 CFR 1.17. (Conditional Extension of Time Fees)
- c. ☐ Fees required under 37 CFR 1.18. (Patent Issue Fees)

21. ☐ Other: \_\_\_\_\_

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Express Mail Label No.	EL 579 029 877 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Thomas P. Glenn  
Assignee: Amkor Technology, Inc.  
Title: Plastic Integrated Circuit Package And Method And Leadframe For Making The Package  
Serial No.: Not Yet Assigned Filing Date: July 13, 2000  
Examiner: Not Yet Assigned Group Art Unit: Not Yet Assigned  
Docket No.: M-5599-2D US

San Jose, California  
July 13, 2000

BOX PATENT APPLICATION  
COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to calculating the fee and examining the application, please amend the application as follows.

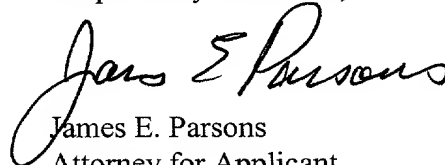
IN THE CLAIMS

Please cancel claims 5, 6, 11, 13-16, 18-21, 27, 31, 34, 35, and 36.

EXPRESS MAIL LABEL NO:

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Respectfully submitted,



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PLASTIC INTEGRATED CIRCUIT PACKAGE AND METHOD  
AND LEADFRAME FOR MAKING THE PACKAGE

Thomas P. Glenn

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FIELD OF THE INVENTION

The present invention is directed toward an improved plastic package for an integrated circuit die, and a method of making such a package.

10

BACKGROUND OF THE INVENTION

Integrated circuit die are conventionally enclosed in plastic packages that provide protection from hostile environments and enable electrical interconnection between the integrated circuit die and printed circuit boards. The elements of such a package include a metal leadframe, an integrated circuit die, bonding material to attach the integrated circuit die to the leadframe, bond wires which electrically connect pads on the integrated circuit die to individual leads of the leadframe, and a hard plastic encapsulant material which covers the other components and forms the exterior of the package.

The leadframe is the central supporting structure of such a package. A portion of the leadframe is

internal to the package, i.e., completely surrounded by the plastic encapsulant. Portions of the leads of the leadframe extend eternally from the package and are used to connect the package externally.

5        Further background information concerning conventional plastic integrated circuit packages and leadframes is contained in chapter 8 of the book Microelectronics Packaging Handbook (1989), which was edited by R. Tummala and E. Rymaszewski, and is  
10       published by Van Nostrand Reinhold, 115 Fifth Avenue, New York, New York.

A problem with conventional plastic packages is that their internal leadframes limit reduction of the size of the packages. Practitioners have attempted to  
15       reduce the size of packages by eliminating internal leadframes, as is shown in U.S. Patent No. 4,530,152 to Roche et al and U.S. Patent No. 5,172,214 to Castro, but these packages have numerous disadvantages. The contacts of the package shown by Roche in the '152  
20       patent have orthogonal side surfaces. Accordingly, the packages are believed to be unreliable because the contacts could easily be pulled from the encapsulant material. The package shown by Castro in the '214

patent has leads which extend into the body of the package from a lower external surface of the package to the top of the die. These leads are large, and have complex bends. Including such leads in a package would  
5 increase manufacturing costs and limit reductions in the lateral size of the package. By contrast, the contacts of the packages within the present invention are simpler, do not have such bends, and allow for packages of smaller lateral size.

10        SUMMARY OF THE INVENTION

The present invention is directed toward improved plastic packages for housing an integrated circuit die, and to leadframes and methods for making such packages. The packages of the present invention  
15 are easier and less expensive to make than conventional plastic packages, and are more reliable and efficiently-sized than conventional packages.

In one embodiment of an assembly method for a package within the present invention, Step 1 provides a  
20 metal leadframe. The leadframe includes a rectangular frame, e.g., a square frame. A substantially planar die pad is within and connected to the frame. A plurality of finger-like rectangular tabs extend from

the frame toward the die pad without contacting the die pad. The number and location of the tabs around the frame may vary. The die pad and the tabs have peripheral side surfaces which include a reentrant  
5 portion(s) and asperities. The reentrant position(s) and asperities enhance the connection of the die pad and tabs to the plastic encapsulating material.

Step 2 places and attaches an integrated circuit to a first surface of the die pad.

10 Step 3 electrically connects a bond wire or an equivalent conductor between each bonding pad of the die and a first surface of one of the tabs.

Step 4 places the leadframe on a flat surface, with the die facing upwards, and applies a viscous  
15 encapsulant material onto the upward facing first surface of the leadframe. The encapsulant material is then hardened. The encapsulant material covers the die, the bond wires, a first surface of the tabs, the first surface of the die pad, the side surfaces of the  
20 die pad and tabs, and all or part of the frames around the die pad. A lower second surface of the leadframe, including a lower second surface of the die pad and tabs, is not covered with encapsulant.

Step 5 plates the exposed surfaces of the leadframe, including the exposed second surfaces of the die pad and tabs with a metal, such as copper, gold, lead-tin solder, tin, nickel, palladium, or any  
5 solderable metal.

Step 6 cuts the encapsulated portions of the leadframe with a saw. In particular, step 6 either obliterates the disposable portions of the leadframe, or severs the disposable portions of the leadframe from  
10 other components of the leadframe, such as the die pad and tabs, which are to be included in the package. Step 6 also trims the encapsulant material and thereby forms the peripheral sides of the package.

A feature the packages built by the above  
15 described method is that the die pad and contacts (i.e., the severed tabs of the leadframe) of the package are located at the lower first surface of the package. The first surfaces and side surfaces of the die pad and tabs are internal to the package, i.e.,  
20 covered with encapsulant material, but the second surfaces of the die pad and tabs are not covered by encapsulant material. The die pad and tabs are isolated from each other by encapsulant material.



In a completed package, only the encapsulant material holds the die pad and contacts to the package. The connection of the encapsulant material to the die pad and contacts is enhanced by the reentrant portion(s) and asperities of the side surfaces of the die pad and contacts. The reentrant portions and asperities of the side surfaces of the die pad and contacts function as encapsulant fasteners or lead locks.

10 Numerous variations of the leadframe, package, and assembly method described above also are described in this application. In one alternative assembly method, a leadframe is provided which allows a plurality of packages to be constructed simultaneously.

15 A leadframe for constructing a plurality of packages simultaneously includes, for example, a matrix of interconnected rectangular frames. A die pad is within and connected to each of the interconnected frames. A set of tabs extend from each frame toward  
20 the sides of the enclosed die pad without contacting the die pad. A subsequent encapsulation step includes applying an encapsulant material onto the surface of the leadframe to which the dies are attached. This

step covers the dies and the side surfaces of the die pads and tabs within a single block of encapsulant material. The encapsulant material is then hardened. A cutting step separates individual packages from each other and from the disposable portions of the leadframe. The cutting step also severs the connection between each of the interconnected frames and the die pad and tabs within each frame.

#### BRIEF DESCRIPTION OF DRAWINGS

10        Figure 1 is a flow chart of a method of making a package.

Figure 2 is a top view of leadframe used for making a package.

15        Figures 3 is an enlarged cross-sectional side view of a circled portion of Figure 2. Figures 3 shows an embodiment of a side surface of a die pad and tab.

Figure 4 is a first alternative embodiment of a side surface of a die pad and tab.

20        Figure 5 is a second alternative embodiment of a side surface of a die pad and tab.

Figure 6 is a third alternative embodiment of a side surface of a die pad and tab.

Figure 7 is a top view of the leadframe of Figure 1 after encapsulation. The dashed lines are cutting paths for a subsequent sawing step.

Figure 8 is a cross-sectional side view of a  
5 completed package.

Figure 9 is a cross-sectional side view of the package of Figure 8 further including solder interconnection bumps on the package contacts.

Figure 10 is a flow chart of a method for making a  
10 plurality of packages simultaneously.

Figure 11 is a top view of a leadframe used for making a plurality of packages simultaneously.

#### DETAILED DESCRIPTION

Figure 1 shows an exemplary method of assembling a  
15 package in accordance with the present invention.

Figure 8 shows a completed package.

Step 1 of Figure 1 provides a metal leadframe.

Figure 2 is a top view of a first embodiment of a metal leadframe 20 in accordance with the present invention.

20 For ease of view, shading is used in Figure 2 to distinguish the metal portions of leadframe 20 from empty spaces between the various elements of leadframe 20.

Leadframe 20 of Figure 2 is planar or substantially planar and is made of a conventional leadframe metal, such as copper or copper alloys, plated copper or plated copper alloys, Alloy 42 (42% nickel, 58% iron), or copper plated steel, depending on the application. The opposing upper and lower surfaces of leadframe 20 may be plated with different metals. For example, the tabs 30 and/ or other portions of leadframe 20 which ultimately are enclosed within the package may be plated with silver, gold, nickel palladium, or copper. Such plating, for example, may enhance attachment of bond wires to tabs 30.

Figure 2 includes dash lines A--A, B--B, C--C, and D--D. These are lines which indicate where leadframe 20 is cut in Step 6 of Figure 1. Step 6 is described below. Figure 2 also includes a circle and dashed line 3-3, which indicate the view of Figure 3.

Leadframe 20 of Figure 2 includes a peripheral rectangular frame 21. Frame 21 consists of four rectilinear members. The two intersecting pairs of parallel members of frame 21 are denoted as members 22 and 22A and 23 and 23A. Artisans should understand that the terms "rectangular" or "rectangle" as used

herein include a square, which is a rectangle with four equivalent sides.

A rectangular die pad 24 is within and connected to frame 21. Die pad 24 has a planar or substantially planar upper first surface 25 and, although it is not shown in Figure 2, an opposite planar or substantially planar lower second surface 26. Die pad 24 also has peripheral side surfaces 27 between upper first surface 26 and lower second surface 26.

A connector 28 connects two parallel side surfaces 27 of die pad 24 to members 22 and 22A of frame 21 of Figure 2. Each connector 28 includes a mushroom-shaped anchor 29, although other shapes may be used for anchor 29.

Three finger-like rectangular tabs 30 are connected to and extend from members 23 and 23A toward an adjacent side surface 27 of die pad 24 without contacting side surfaces 27. As a result of this configuration, the completed package will have a single row of three contacts on two parallel sides of the package. Tabs 30 ultimately are severed from members 23 and 23A along cut lines C--C and D--D of Figure 2, and become the contacts of the package.

The number, location, and shape of tabs 30 may vary. For example, instead of having tabs 30 only on members 23 and 23A of frame 21 of leadframe 20, as in Figure 2, sets of tabs 30 may be placed on all four members of frame 21. This alternative embodiment would result in the formation of a quad package.

Each tab 30 of Figure 2 has a planar or substantially planar upper first surface 31 and, although it is not shown in Figure 2, an opposite planar or substantially planar lower second surface 32. Each tab 30 also has three peripheral side surfaces 33 between upper first surface 31 and lower second surface 32.

Figures 3-6 show an enlarged cross-sectional side view of the circled portion of Figure 2 along line 3-3. In particular, Figures 3-6 show, in accordance with the present invention, a side surface 27 of a die pad 24 and a side surface 33 of a tab 30 of leadframe 20 of Figure 2.

Side surface 27 of die pad 24 and side surface 33 of tab 30 of Figure 3 have reentrant portions. In particular, the upper and lower portions of side surfaces 27 and 33 are reentrant such that there is a

central peak 34 which extends outward from side surfaces 27 and 33 of die pad 24 and tab 30, respectively. Encapsulant material flows into the reentrant portions of side surfaces 27 and 33. Central  
5 peak 34 extends into the encapsulant material.

The reentrant portions of side surfaces 27 of die pad 24 and side surfaces 33 of tabs 30 of Figure 3 have the function, in a completed package, of enhancing the connection between the encapsulating material, on the  
10 one hand, and die pad 24 and the contacts of the package (i.e., severed tabs 30), on the other hand.

In addition to having reentrant portions, side surface 27 of die pad 24 and side surface 33 of tab 30 of Figure 3 have a roughly-textured surface which  
15 includes numerous asperities. Encapsulant material flows into the areas of the asperities. The asperities also enhance the connection between the encapsulant material and die pad 24 and the contacts of the package (i.e., the severed tabs 30).

20 Figure 4 shows a first alternative profile for side surfaces 27 of die pad 24 and side surfaces 33 of tabs 30 of leadframe 20 of Figure 2. In the embodiment of Figure 4, side surfaces 27 and 33 each have a

central depression 35 and a roughly-textured surface which includes numerous asperities. Encapsulant material flows into central depression 35 and in the areas of the asperities. The reentrant portion and  
5 asperities of side surfaces 27 and 33 of Figure 4 have the function, in a completed package, of enhancing the connection between the encapsulant material and die pad 24 and the contacts of the package (i.e., the severed tabs 30).

10 Figure 5 shows a second alternative profile for side surfaces 27 of die pad 24 and side surfaces 33 of tabs 30 of leadframe 20 of Figure 2. In the embodiment of Figure 5, side surfaces 27 and 33 include a rounded lip 36 adjacent to upper surface 25 and 31 of die pad  
15 24 and tab 30, respectively. Lip 36 has a roughly-textured surface which includes numerous asperities. Side surfaces 27 and 33 also have a reentrant orthogonal portion 37 beneath lip 36, adjacent to lower second surface 29 and 32 of die pad 24 and tab 30,  
20 respectively. Encapsulant material flows beneath lip 36 and into the area of the asperities. Like the embodiments of Figures 3 and 4, the reentrant portions and asperities of side surface 27 of die pad 24 and



side surface 33 of tab 30 of Figure 5 have the function, in a completed package, of enhancing the connection between the encapsulant material and die pad 24 and the contacts of the package (i.e., tabs 30 after they are severed from members 23 and 23A).

Figure 6 shows a third alternative for side surfaces 27 of die pad 24 and side surfaces 33 of tabs 30 of leadframe 20 of Figure 1. In this embodiment, side surfaces 27 and 33 each include a rectangular lip 38 adjacent to upper surface 25 and 31 of die pad 24 and tab 30, respectively. Side surfaces 27 and 33 also have a reentrant orthogonal portion 39 beneath lip 38 adjacent to lower second surface 29 and 32 of die pad 24 and tab 30, respectively. Encapsulant material flows beneath lip 38. Like the embodiments of Figures 3-5, the reentrant portions of side surface 27 of die pad 24 and side surface 33 of tab 30 of Figure 6 have the function, in a completed package, of enhancing the connection between the encapsulant material and die pad 24 and the contacts of the package (i.e., severed tabs 30).

As discussed above, Step 1 of Figure 1 provides a metal leadframe having features like those shown in

Figure 2 and either Figure 3, 4, 5, or 6, or equivalents thereof. Leadframe 20 of Figure 2 is formed from rolled strip metal stock by wet chemical etching or mechanical stamping using progressive dies.

5       As is well known, chemical etching (also known as chemical milling) is a process that uses photolithography and metal-dissolving chemicals to etch a pattern into a metal strip. The photoresist is exposed to ultraviolet light through a photo mask  
10       having a desired pattern, and is subsequently developed and cured. Chemicals are sprayed or otherwise applied to the masked strip, and exposed portions of the strip are etched away, leaving the desired pattern.

      As is also well known, progressive stamping uses  
15       sets of progressive dies to mechanically remove metal from a metal strip. Each of a plurality of stamping stations uses one of the dies to punch a distinct small area of metal from the strip as the strip moves through the stations.

20       A leadframe 20 having side surfaces like Figure 3 can be formed by chemically etching the rolled strip metal stock from both sides using a conventional liquid

etchant. The etch process is stopped early so that there is an underetching of all of the side surfaces of the components of leadframe 20, including side surfaces 27 of die pad 24 and side surfaces 33 of tabs 30, compared to the time it would take to form vertical side surfaces. The size and shape of central peak 34 of Figure 2 is controlled by the amount of underetching.

A leadframe 20 having side surfaces like Figure 4 can be formed by chemically etching the rolled strip metal stock from one side using a conventional liquid etchant. The etch process is continued beyond the time required to form orthogonal side surfaces for the components of leadframe 20. The size and shape of central depression 35 of Figure 3 is controlled by the amount of overetching.

A leadframe 20 having side surfaces like Figure 5 can be formed in a two step process. The first step of such a process involves forming a leadframe 20 by chemical etching or progressive stamping so that the side surfaces of the components of leadframe 20, including die pad 24 and tabs 30, have an orthogonal profile. The second step involves coining the upper

first surface of the leadframe 20, that is, applying a high pressure impact to the upper first surface of the leadframe 20. This step deforms the side surfaces of leadframe 40 adjacent to the impacted surface so that  
5 the rounded, asperity-marked protruding lip 36 of Figure 5 is formed.

A leadframe 20 having side surfaces like Figure 6 can be formed by progressive stamping. The side surfaces of the components of leadframe 20, including  
10 side surfaces 27 of die pad 24 and the side surfaces 33 of tabs 30, can be provided with a rectangular lip 38 and a reentrant orthogonal portion 39 by including intermediate stamping steps which do not fully cut through the rolled strip metal stock before finally  
15 cutting through the rolled strip sheet. The intermediate stamping steps and the final cutting steps combine to form the rectangular, protruding lips 38 of side surfaces 27 and 33 of Figure 5.

Step 2 of Figure 1 places an integrated circuit  
20 die onto upper first surface 25 of die pad 24. The placement and attachment of the die onto die pad 24 may be performed using a conventional die attach machine and conventional die attach adhesives. During Step 2

and the subsequent assembly steps, leadframe 20 of Figure 2 is grounded to protect against electrostatic discharge ("ESD").

Step 3 of Figure 1 electrically connects a  
5 conductive metal bond wire between individual bonding pads on the integrated circuit die and the upper first surface 31 of individual tabs 30 on leadframe 20 of Figure 2. Tabs 30 ultimately become contacts in the completed package, after tabs 30 are severed from  
10 members 23 and 23A of frame 21. Conventional bond wire attachment equipment may be used for Step 3. Leadframe 20 of Figure 2 is grounded during this wiring step to prevent damage to the integrated circuit dies due to electrostatic discharge. At the completion of Step 3,  
15 each bonding pad of each die is electrically connected to a tab 30 of leadframe 20 of Figure 1, which is grounded. Tabs 30 of leadframe 20 are all shorted together, which facilitates ESD protection.

In Step 4 of Figure 1, the lower second surface of  
20 leadframe 20 of Figure 2 is placed on a flat surface, and a viscous adhesive encapsulating material is applied onto the upward facing upper first surface of leadframe 20. The encapsulating material is applied so

that the encapsulating material covers: the integrated circuit die; the bond wires; any exposed peripheral portions of upper first surface 25 of die pad 24 around the die; side surfaces 27 of die pad 24; upper first surface 31 of tabs 30; side surfaces 33 of tabs 33; and part or all of the width of members 22, 22A, 23, and 23A of frame 21. The encapsulant material also fills the empty spaces between the components within frame 21 of leadframe 20. The encapsulant material does not, however, cover lower second surface 26 of die pad 24 or lower second surfaces 32 of tabs 30 of Figure 2. In an alternative embodiment, die pad 24 may be up set during the encapsulation step so that a thin layer of encapsulant material forms under lower second surface 26 of die pad 24. If such a step were used, die pad 24 would be completely internal to the package. Finally, the encapsulant material is hardened.

There are several methods by which Step 4 of Figure 1 may be accomplished, depending on the application. For example, as a first step, leadframe 20 of Figure 2 is placed on a horizontal surface. As a second step, a contiguous bead of a conventional hardenable viscous adhesive material, such as HYSOL

4451 epoxy from the Dexter-Hysol Company of City of Industry, California, is applied onto the upper first surface of side members 22, 22A, 23, and 23A of frame 21 of leadframe 20 of Figure 2, forming a closed rectangular dam. As a third step, the dam is solidified, such as by heating at 150 °C for one hour. As a fourth step, a conventional hardenable viscous adhesive material suitable for encapsulating packages, such as HYSOL 4450 encapsulant, is applied within the dam so that the incomplete package within the dam is covered with encapsulant material. As a final step, the encapsulant material is hardened, such as by heating at 150 °C for one hour, forming a single solid block of encapsulant material above and on leadframe 20, including on its side surfaces.

Alternatively, Step 4 of Figure 1 may be accomplished using conventional plastic molding techniques. In such a method, leadframe 20 of Figure 2 is placed in a mold, and a single block of solid molded encapsulant material is formed above and on leadframe 20, including on its side surfaces. The encapsulant material may be a conventional plastic molding compound applied using conventional techniques. Example molding

compounds include NITTO MP-8000AN molding compound from the Nitto Company of Japan, and EME 7351 UT molding compound from the Sumitomo Company of Japan.

Conventional gates may be formed in leadframe 20 to  
5 assist in the molding process.

In Step 5 of Figure 1, the portions of leadframe 20 of Figure 2 which are not covered with the encapsulant material, including lower second surface 26 of die pad 24 and lower second surfaces 32 of tabs 30,  
10 are plated using a conventional plating metal compatible with printed circuit boards. For example, exposed second surfaces 26 and 33 of die pad 24 and tabs 30, respectively, may be plated with gold, nickel palladium, inconel, lead tin solder, or tantalum,  
15 depending on the application. The plating step is facilitated by the electrical interconnection of the components of leadframe 20.

Figure 7 is a top view of leadframe 20 of Figure 2 after the completion of Steps 1-5 of Figure 1. A  
20 rectangular block of hardened encapsulant material 40 covers the upper first surface of leadframe 20. Although not shown, encapsulant material 40 also covers side surfaces 27 and 33 of die pad 24 and tabs 30,



respectively, of leadframe 20. The block of encapsulant material 40 in Figure 7 covers a portion of the width of members 22, 22A, 23, and 23A of frame 21 of leadframe 20. The peripheral portions of members 22, 22A, 23, and 23A of frame 21 remain exposed.

Alternatively, encapsulant material 40 could be deposited over the entire upper first surface of leadframe 20. As a second alternative, encapsulant material 40 could be deposited within frame 21 so that tabs 30 are covered, but members 22, 22A, 23, and 23A are not covered.

Step 6 of Figure 1 cuts leadframe 20 of Figure 7 in situ. Referring to Figures 2 and 7, Step 6 severs the connection between tabs 30 and members 23 and 23A of frame 21 of leadframe 20. Step 2 also severs connectors 28 between die pad 24 and members 22 and 22A of frame 21 of leadframe 20. Step 6 also cuts encapsulant material 40, forming vertical external side surfaces of the package. Finally, Step 6 completes the formation of the package by cutting a completed package away from the disposable portions of leadframe 20.

Step 6 may be performed using a saw or other shearing apparatus. To perform Step 6 using a saw, the

encapsulated leadframe 20 of Figure 7 is inverted and placed on sticky film. Using the exposed portions of leadframe 20 as a guide (see Figure 2), a conventional wafer saw is used to saw a completed package from the encapsulated leadframe 20. Criss-crossing rectilinear cuts are made along dashed lines A--A, B--B, C--C, and D--D of Figures 2 and 7 so that the disposable portions of leadframe 20, including side members 22, 22A, 23, and 23A of frame 21, connectors 28 and anchors 29, are cut away from the package, isolated within encapsulant material 40, or obliterated by the saw. The cutting path of the saw and/or the width of the saw blade should be selected so that the connections between tabs 30 and members 23 and 23A are severed and side members 22, 22A, 23, and 23A are cut away or obliterated, but all or most of each tab 30 remains intact.

Figure 8 is a cross-sectional side view of an exemplary package 50 made from leadframe 20 of Figure 2 according to Steps 1-6 of Figure 1. Package 50 has a planar or substantially planar external upper first surface 51, and an opposite planar or substantially planar external lower second surface 52. Orthogonal external package sides 57 are at the periphery of

package 50 between upper first surface 51 and lower second surface 52. Sides 57 were formed during Step 6, when encapsulant material 40 and tabs 30 were cut.

Lower second surface 52 of package 50 of Figure 8 consists of die pad 24, a plurality of peripheral contacts 53, and hardened encapsulant material 40. Die pad 24 and each contact 53 are like islands at the lower external second surface 52 of package 50. They are physically separated from each other by encapsulant material 40.

Die pad 24 and contacts 53 of Figure 8 are vestiges of leadframe 20 of Figure 2. Referring to Figures 2 and 8, contacts 53 of package 50 of Figure 8 were formed when the connections between tabs 30 and members 23 and 23A were severed by the saw during Step 6.

Die pad 24 of Figure 8 is rectangular and is located at lower second surface 52 of package 50. Die pad 24 includes a planar or substantially planar upper first surface 25, an opposite planar or substantially planar second surface 26, and peripheral side surfaces 27. Second surface 26 of die pad 24 is in the same plane as lower second surface 52 of package 50 in

Figure 8, although in alternative embodiments, die pad 24 may be set up into encapsulant material 40.

Although not fully shown in Figure 8, rectangular die pad 24 has four side surfaces 27 (only two are shown). Each side surface 27 of die pad 24 has a reentrant portion(s), as exemplified by Figures 3-6. In addition, side surface 27 may have asperities, as exemplified by Figures 3-5.

In Figure 8, integrated circuit die 56 is on and attached to upper first surface 25 of die pad 24. Peripheral portions of upper first surface 25 are covered by encapsulant material 40. Side surfaces 27 of die pad 24 also are covered by encapsulant material 40. Lower second surface 26 of die pad 24 is not covered encapsulant material 40, but rather is exposed at lower external surface 52 of package 50. In an alternative embodiment (not shown), die pad 24 may be entirely internal to encapsulant material 40 of package 50.

Two contacts 53 are shown in package 50 of Figure 8, but since package 50 was constructed from leadframe 20 of Figure 2, it should be understood that package 50 has a set of three contacts 53 on two sides

57 of package 50. In alternative embodiments, package 50 could be formed with a different number or arrangement of contacts, depending on the application.

Each contact 53 of Figure 8 has a substantially rectangular perimeter and is located at the lower second surface 52 of package 50. Each contact 53 includes a planar or substantially planar upper first surface 31, an opposite planar or substantially planar second surface 32, three internal side surfaces 33 (only one is shown in Figure 8) having reentrant portions, and one external orthogonal side surface 55. Second surface 32 of contact 53 is in the same plane as lower second surface 52 of package 50.

First surface 31 and side surfaces 33 of contacts 53 are covered with an encapsulant material. Second surface 32 and external side surface 55 of contacts 53 are not covered with encapsulant material.

Orthogonal external side surfaces 55 of contacts 53 of Figure 8 were formed during Step 6 of Figure 1 when the saw cut the connections between tabs 30 and members 23 and 23A of leadframe 20 of Figure 2. Accordingly, the external side surface 55 of each contact 53 has a vertical profile which is the same

plane as the corresponding vertical side 57 of package 50.

Although not shown in Figure 8, the three internal side surfaces 33 (only one is shown) of each contact 53 have reentrant portions, as exemplified by Figures 3-6. In addition side surfaces 33 may have asperities, as exemplified by Figures 3-5. Both the reentrant portion(s) and asperities of contacts 53 enhance the connection between contacts 53 and encapsulant material 40 of package 50 of Figure 8.

The perimeter of contacts 53 need not be substantially rectangular in shape. For example, if tabs 30 of leadframe 20 of Figure 2 had a circular perimeter, then contacts 53 would have a largely circular perimeter with a rectilinear portion formed during the cutting of tab 30 from leadframe 20 in Step 6.

A bond wire 58 is connected between each bonding pad 56a of die 56 and the upper first surface 31 of each contact 53. Bond wire 58 electrically connects individual bonding pads 56a of die 56 to individual contacts 53.

Second surface 32 of contacts 53 of Figure 8 may be directly connected to an external printed circuit board, as in an LCC package. Alternatively, a solder interconnection bump may be formed on contacts 53 for physically and electrically connecting package 50 to a printed circuit board. Figure 9 shows a solder interconnection bump 60 formed on lower second surface 32 and external side surface 55 of each contact 53 of package 50 of Figure 8.

10 In an alternative embodiment, second surface 26 of die pad 24 also may be connected, such as by solder paste, to the printed circuit board to facilitate package cooling. The cooling occurs by thermal conduction.

15 Figure 10 is a flow chart for an alternative assembly method, in accordance with the present invention, for constructing a package like that of Figure 8. In the method of Figure 10, a plurality of packages are constructed simultaneously. The basic steps of the Figure 10 process are the same as the Figure 1 process.

Step 1 of Figure 10 provides a thin metal leadframe which includes a plurality of interconnected

rectangular frames in a matrix. A die pad is provided within each frame.

Figure 11 shows an exemplary metal leadframe 70, in accordance with the present invention, suitable for Step 1 of Figure 10. Shading is used in Figure 11 to distinguish metal portions of leadframe 70 from empty space between the components of leadframe 70.

Leadframe 70 of Figure 11 is planar or substantially planar and is formed of metal. The metals and methods used for constructing leadframe 70 are the same as those described above for leadframe 20 of Figure 2.

Leadframe 70 of Figure 11 includes a disposable rectangular outer frame 71. Outer frame 71 consists of four intersecting members, denoted as members 72-75. Member 72 is parallel to member 74, and member 73 is parallel to member 75.

Within outer frame 71 of Figure 11 are four interconnected rectangular frames in a two by two matrix. These frames are formed by the intersection of three disposable strips 76 and three disposable strips 77. Each of the four interconnected frames of Figure 11 has the same basic features as frame 21 of



Figure 2. Accordingly, the same reference numbers will be used, where applicable, and associated discussion will be abbreviated.

A rectangular die pad 24 is within and connected  
5 to each of the four frames formed by strips 76 and 77  
of Figure 11. As in Figure 2, each die pad 24 of  
Figure 11 has four side surfaces 27. Each side surface  
27 has a reentrant portion(s), such as in the examples  
of Figures 3-6. Side surfaces 27 also may include  
10 asperities, such as those shown in Figures 3-5.

Three parallel strips 76 are within and connected  
to frame 71 of Figure 11. A first strip 76 is adjacent  
to, parallel to, and connected to member 72 of frame  
71. A second strip 76 is adjacent to, parallel to, and  
15 connected to member 74 of frame 71. A third strip 76 is  
located in the center of frame 71 between juxtaposed  
pairs of die pads 24. Each strip 76 of Figure 11 is  
connected to each of the die pads 24 which are adjacent  
to that particular strip 76. A disposable mushroom-  
20 shaped anchor 29 connects each strip 76 to each  
adjacent die pad 24. Two disposable connectors 78  
connect member 72 to its adjacent strip 76, and two  
connectors 78 connect member 74 to its adjacent strip

76. The number and locations of connectors 76 may vary.

Three parallel strips 77 also are within and connected to frame 71 of Figure 11. One strip 77 is adjacent to, parallel to, and connected to side members 73 and 75 of frame 71. A disposable connector 78 connects members 73 and 75 to their respective adjacent strip 77. A third strip 77 is located in the center of frame 71 between juxtaposed pairs of die pads 24.

10 The intersecting ends of the peripheral strips 76 and 77 of Figure 11 are connected to the inner corners of outer frame 71. A gate 79 extends at approximately a 45 degree angle from each of the four inner corners of frame 71 and connects to the interconnected ends of peripheral strips 76 and 77. Gate 79 is useful for the introduction of molding compound into a mold, where molding is the chosen method of encapsulation.

Central strip 76 of Figure 11 intersects central strip 77 at the center of leadframe 70. The ends of central strips 76 and 77 intersect the peripheral strips 77 and 76, respectively.

In Figure 11, a plurality of straight, evenly-spaced, finger-like, rectangular tabs 30 extend in sets

of five from each strip 77 toward the sides of each of the die pads 24 adjacent that particular strip 77.

Tabs 30 do not contact die pads 24. The central strip 77 that is between juxtaposed pairs of die pads 24 has mirror-image sets of five tabs 30 which extend in opposite directions toward each of the juxtaposed die pads 24. Each tab 30 will ultimately form a contact 53 of package 50 of Figure 8.

Each tab 30 of Figure 11 has three side surfaces 33 which have a reentrant portion(s), such as shown in Figures 3-6. Side surfaces 33 of tabs 53 also may include asperities, such as those shown in Figures 3-5. The reentrant portions and asperities of side surfaces 33 of tabs 30 enhance the connection between encapsulant material 40 and contacts 53 (i.e., severed tabs 30) of a completed package 10 of Figure 8.

Step 2 of Figure 10 places and attaches an integrated circuit die 56 on upper first surface 25 of each die pad 24 of leadframe 70 of Figure 11, as described above for Step 2 of Figure 1.

Referring to Figures 8 and 11, Step 3 of Figure 10 electrically connects a conductive metal bond wire 58 between each bonding pad 56a on each integrated circuit

die 56 attached to leadframe 70 and a tab 30. Bond wire 58 is connected to the first surface 31 of each tab 30. The methods for Step 3 of Figure 11 are the same as described above for Step 3 of Figure 1.

- 5        Step 4 of Figure 10 covers each incomplete package of leadframe 70 of Figure 11, including all of the dies 56, with a conventional viscous, adhesive encapsulant material. The methods and materials used for Step 4 of Figure 10 are the same as for Step 4 of Figure 1,
- 10       except that the encapsulant material is applied onto all of the incomplete packages 50 of leadframe 70 of Figure 11. The encapsulant material covers the upper first surface of leadframe 70, as well as side surfaces 27 and 33 of die pads 24 and tabs 33, respectively.
- 15       The encapsulant material is then hardened into a single block which covers all of the incomplete packages of leadframe 70 of Figure 11, as well as all or part of the width of members 72-75 of frame 71 of leadframe 70. Again, the lower second surface of leadframe 70,
- 20       including lower surfaces 26 and 32 of die pads 24 and tabs 30, respectively, is not covered by encapsulant material, but instead remains exposed.

Step 5 of Figure 10 plates the exposed lower surface of leadframe 70 of Figure 11, including lower second surfaces 26 and 32 of die pads 24 and tabs 30, respectively, with a conventional plating metal. This  
5 step is accomplished as described above for Step 5 of Figure 1.

Step 6 of Figure 10 cuts leadframe 70 of Figure 11 after the encapsulation step. Encapsulated leadframe 70 is cut in situ, similar to leadframe 20 of Figure 7.  
10 The disposable portions of leadframe 70 are either severed from the packages, obliterated, or isolated by encapsulant material from the other components of package 50 of Figure 8. The requirements of and methods used for Step 6 of Figure 10 are basically the  
15 same as described above for Step 6 of Figure 1, except that more cuts have to be made because leadframe 70 of Figure 11 is bigger and has more components than leadframe 20 of Figure 2.

Step 6 of Figure 10 severs the connection between  
20 tabs 30 and strips 77 of leadframe 70. This cut forms the isolated, individual contacts 53 shown in package 50 of Figure 8. Step 6 also severs the connection between anchors 29 and strips 76. This cut physically

isolates die pads 24 within the encapsulant material.

Step 6 also cuts through the single block of encapsulant material formed during Step 4 to form four packages 50 from leadframe 70 of Figure 11.

5           Step 6 may be performed using a saw or other cutting apparatus. Where a saw is used for Step 6, the saw is moved along strips 76 and 77 (See Figure 11). The saw blade used should be wider than strips 76 and 77 of Figure 11, but narrower than the combined width  
10 of central strip 77 and the back-to-back tabs 30. As a result, moving the saw blade along strips 76 and 77 will obliterate strips 76 and 77, but will not obliterate tabs 30. As discussed above, the surface area of tabs 30 must be maintained because the severed  
15 tabs 30 become contacts 53 in package 50 of Figure 8.

          An exemplary method of accomplishing Step 6 of Figure 10 includes a first step of inverting the encapsulated leadframe 70 and placing it on sticky paper. Using the exposed portions of leadframe 70 of  
20 Figure 11 as a pattern, three parallel cuts are made, each of which goes through side members 73 and 75 and along and through the length of a strip 76 of leadframe 70. These three cuts form two of the four external

side surfaces 57 of package 50 of Figure 8; obliterate strips 76; and sever the connections between die pads 24 and strips 76.

Next, the encapsulated leadframe 70 is rotated 90  
5 degrees, and three parallel cuts are made perpendicular to the original three cuts. Each of these latter three cuts goes through side members 72 and 74 and along and through the length of a strip 77. These latter three cuts also form the remaining two external side surfaces  
10 57 of package 50 of Figure 8. Since the width of the saw blade is selected to be wider than strips 76 and 77, but narrower than the combination of central strip 77 and tabs 30, the latter three cuts obliterate strips 77 but do not obliterate the tabs 30 which are attached  
15 to strips 77.

The six cuts described above complete the formation of the four packages 50 from leadframe 70 of Figure 11 by separating the completed packages from one another and from the disposable portions of leadframe  
20 70.

Artisans will appreciate that numerous variations of the packages, leadframes, and assembly methods described above are possible. As one example, changes

can be made to leadframe 70 of Figure 11 in order to change the size, shape and numbers of the packages 50 (Figure 7) formed from leadframe 70. For example, instead of simultaneously forming four packages using a leadframe like leadframe 70 of Figure 11, the size of the leadframe may be adjusted so that two, eight, sixteen, forty-eight or some other number of packages are formed simultaneously. As another example, one may multiply the number of packages formed simultaneously by forming several leadframes 70 adjacent to each other on a single strip of rolled stock, and processing all of the leadframes 70 on the strip simultaneously. As another example, the peripheral shapes of die pads 24 and tabs 30 may be changed from rectangular to some other shape.

In addition, the profiles of side surfaces 27 and 33 of die pads 24 and tabs 30, respectively, can be altered from the embodiments of Figures 3-6, provided that the function of enhancing the connection between encapsulant material 40 and the die pads 24 and contacts 53 of packages 50 of Figure 8 is maintained.

Leadframe 70 of Figure 11 may be modified in other ways as well. For example, the peripheral strips 76



and 77 that are adjacent to members 72-75, may be omitted. In such a case, the anchors 29 of Figure 11 would be attached to members 72 and 74, and tabs 30 would be attached to members 73 and 75 of frame 71 of  
5 leadframe 70.

As a final example, instead of forming a single block of encapsulant material over all of the dies and incomplete packages of leadframe 70 of Figure 11, a mold having individualized cavities for forming a block  
10 of encapsulant material above each the four interconnected frames and die pads 24 of leadframe 70 may be used. In such a case, less encapsulant material would be cut in Step 6 of Figure 10.

The above description of embodiments of this  
15 invention is intended to be illustrative and not limiting. Other embodiments of this invention will be obvious to those skilled in the art in view of the above disclosure.

CLAIMS

1. A package for an integrated circuit die  
comprising:

an integrated circuit die;

5 a plurality of metal contacts adjacent to  
peripheral sides of the package;

wherein each contact has a substantially  
planar first surface, an opposite substantially  
planar second surface, and a side surface between  
10 said first and second surfaces;

wherein said first surface has a bond wire  
connected thereto, said second surface is exposed  
at a first external surface of the package, and  
said side surface includes a reentrant portion;  
15 and

an encapsulant material which covers the  
integrated circuit die and the reentrant portion  
of the side surface of each contact.

20 2. The package of claim 1, further comprising:  
a die pad having a substantially planar first  
surface, an opposite substantially planar second

surface, and a side surface between the first surface and the second surface; and

wherein the side surface of the die pad has a reentrant portion, and the encapsulant material covers the reentrant portion of the side surface of the die pad.

3. The package of claim 2, wherein the second surface of the die pad is exposed at the first external surface of the package.

4. The package of claim 2, wherein the die pad is internal to the package.

5. The package of claim 3, wherein each package has a rectangular perimeter and has four peripheral sides, and said contacts are aligned in a row along two opposite peripheral sides of the package.

6. The package of claim 3, wherein said contacts are aligned in a row along each of the four peripheral sides of the package.

7. The package of claim 3, wherein the side surfaces of the die pad and contacts include a central peak.

5

8. The package of claim 3, wherein the side surfaces of the die pad and contacts include a central depression.

10

9. The package of claim 3, wherein the side surfaces of the die pad and contacts include a lip adjacent to the first surface of the die pad and contacts, respectively, and a reentrant orthogonal portion adjacent to said lip.

15

10. The package of claim 3, wherein the side surfaces of the die pad and contacts include asperities.

20

11. A package for an integrated circuit die comprising:

an integrated circuit die;

a plurality of metal contacts adjacent to peripheral sides of the package;

wherein each contact has a substantially planar first surface, an opposite substantially planar second surface, and a side surface between  
5 said first and second surfaces;

wherein said first surface has a bond wire connected thereto, said second surface is exposed at a first external surface of the package, and  
10 said side surface includes a reentrant portion;

a die pad upon which the integrated circuit die is placed, said die pad including a peripheral side surface; and

an encapsulant material which covers the  
15 integrated circuit die and the side surfaces of each contact and the die pad; and

wherein the side surface of each contact includes a means for enhancing the connection between the encapsulant material and the contact,  
20 and the side surface of the die pad includes a means for enhancing the connection between the encapsulant material and the die pad.

12. A leadframe for making an encapsulated integrated circuit die package comprising:

a frame;

5 a die pad within and connected to the frame,

wherein said die pad has a side surface;

a plurality of substantially planar tabs which extend from the frame toward the die pad without contacting the die pad, wherein each tab  
10 includes a side surface; and

wherein the side surfaces of the die pad and tabs include a reentrant portion.

13. The leadframe of claim 12, wherein the side  
15 surfaces of the die pad and tabs include a central peak.

14. The leadframe of claim 12, wherein the side surfaces of the die pad and tabs include a central  
20 depression.

15. The leadframe of claim 12, wherein the die pad and tabs have a first surface, and the side

surfaces of the die pad and tabs include a lip adjacent to the first surface, and a reentrant orthogonal portion adjacent to said lip.

5           16. The leadframe of claim 12, wherein the side surfaces of the die pad and contacts include asperities.

10           17. A leadframe for making a plurality of encapsulated integrated circuit die packages comprising:

            a plurality of die pads, wherein each die pad has a side surface;

15           a plurality of interconnected frames in a matrix, wherein one of said die pads is within and connected to each of the frames; and

            a plurality of substantially planar tabs which extend from each frame toward the die pad within the particular frame without contacting the die pad; and

20

            wherein the side surfaces of the die pads and tabs include a reentrant portion.

18. The leadframe of claim 17, wherein the side surfaces of the die pads and tabs include a central peak.

5

19. The leadframe of claim 17, wherein the side surfaces of the die pads and tabs include a central depression.

10

20. The leadframe of claim 17, wherein the die pads and tabs have a first surface, and the side surfaces of the die pads and tabs include a lip adjacent to said first surface, and a reentrant orthogonal portion adjacent to said lip.

15

21. The leadframe of claim 17, wherein the side surfaces of the die pads and contacts include asperities.

20

22. A method of making an integrated circuit die package comprising:

providing a substantially planar metal leadframe, said leadframe including a frame;



wherein a substantially planar die pad is within and connected to the frame, and substantially planar tabs extend from the frame toward the die pad without contacting the die pad; and

wherein the die pad and tabs each have a first surface, an opposite second surface, and a side surface, and the side surfaces of the die pad and tabs include a reentrant portion;

placing an integrated circuit die on the first surface of die pad;

electrically connecting the integrated circuit die to the first surface of the tabs;

applying an encapsulant material onto the frame so that the integrated circuit die, the first surfaces of the die pad and tabs, and the side surfaces of the die pad and tabs are covered with the encapsulant material, but the second surfaces of the tabs are not covered;

hardening the encapsulant material; and

cutting the encapsulated frame so that the die pad and tabs are severed from the frame, a

completed package including a die, a die pad, and severed tabs is detached from the leadframe, and said severed tabs are adjacent to peripheral sides of the package.

5

23. The method of claim 22, wherein the second surface of the die pad also is not covered with encapsulant material.

10

24. The method of claim 23, further including cutting encapsulant material to form the peripheral sides of the package.

15

25. The method of claim 24, wherein the cutting is done by sawing.

20

26. The method of claim 23, further comprising plating the second surfaces of the die pad and tabs with a metal after applying the encapsulant material.

27. The method of claim 22, further comprising connecting the leadframe to electrical ground.

28. A method of making a plurality of integrated circuit packages comprising:

providing a substantially planar metal  
5 leadframe, said leadframe including a plurality of interconnected frames in a matrix;

wherein a substantially planar die pad is within and connected to each of the frames;

10 wherein a plurality of substantially planar tabs extend from each frame toward the die pad within the frame without contacting the die pad;

wherein the die pads and tabs each  
15 have first surface, an opposite second surface, and a peripheral side surface; and

wherein the side surfaces of the die pads and tabs include a reentrant  
20 portion;

placing an integrated circuit die on the first surface of each die pad;

electrically connecting each integrated circuit die to the first surface of the tabs which extend toward the particular die;

5           applying an encapsulant material onto each of the frames so that the integrated circuit dies, the first surfaces of the die pads and the tabs, and the side surfaces of the die pads and tabs are covered with the encapsulant material, but the second surfaces of the tabs are not covered;

10           hardening the encapsulant material; and

          cutting the encapsulated frames so that the die pads and tabs are severed from their respective frames, a plurality of completed packages each including a die, a die pad, and  
15           severed tabs are formed, and the second surfaces of the severed tabs of each package are adjacent to peripheral sides of the package.

29. The method of claim 28, wherein the second  
20           surface of the die pad also is not covered with encapsulant material.

420000

30. The method of claim 29, wherein the cutting  
is done with by sawing.

31. The method of claim 29, further comprising  
5 plating the second surfaces of the die pads and tabs  
with a metal after applying the encapsulant material.

32. The method of claim 28, wherein a single  
block of encapsulant material covers all of the dies,  
10 and further including cutting encapsulant material to  
form the peripheral sides of the packages.

33. The method of claim 28, wherein each die is  
within a separate unit of encapsulant material.

15

34. The method of claim 28, further comprising  
connecting the leadframe to electrical ground.

35. A method of making a leadframe comprising:  
20 chemically etching a metal sheet to form a  
frame;  
wherein a die pad is within and connected to  
the frame, tabs extend from the frame toward the

die pad without contacting the die pad, and the die pad and tabs each have a peripheral side surface which includes a reentrant portion.

- 5        36. A method of making a leadframe comprising:  
progressively stamping a metal sheet to form  
a frame;

10        wherein a die pad is within and connected to the frame, tabs extend from the frame toward the die pad without contacting the die pad, and the die pad and tabs each have a peripheral side surface which includes a reentrant portion.

PLASTIC INTEGRATED CIRCUIT PACKAGE AND METHOD  
AND LEADFRAME FOR MAKING THE PACKAGE

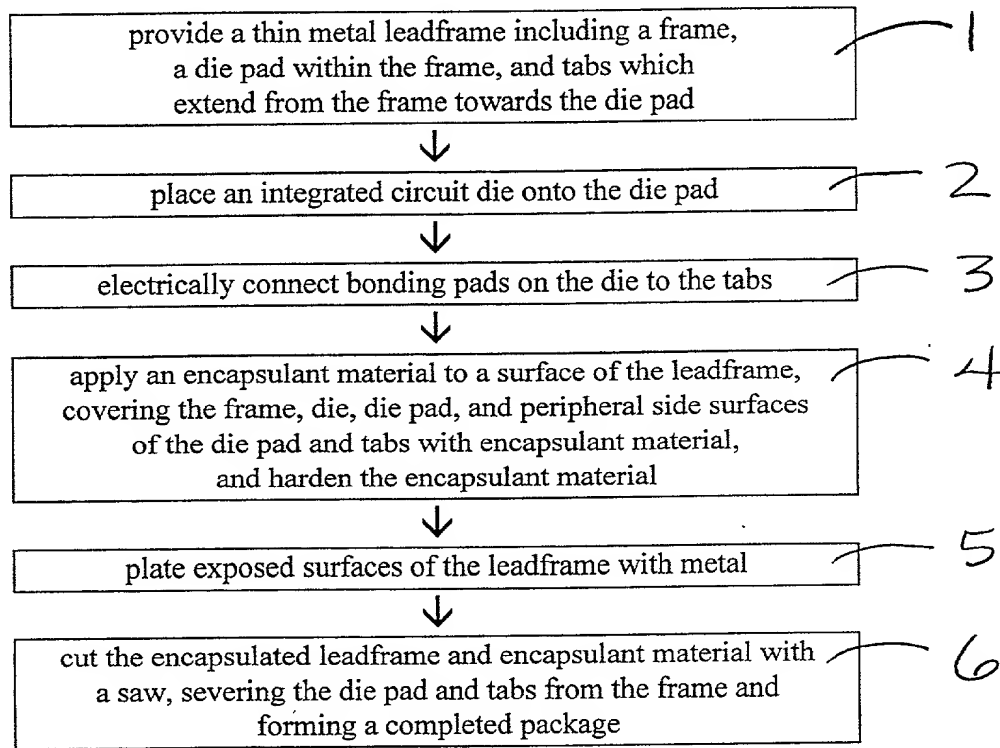
Thomas P. Glenn

ABSTRACT

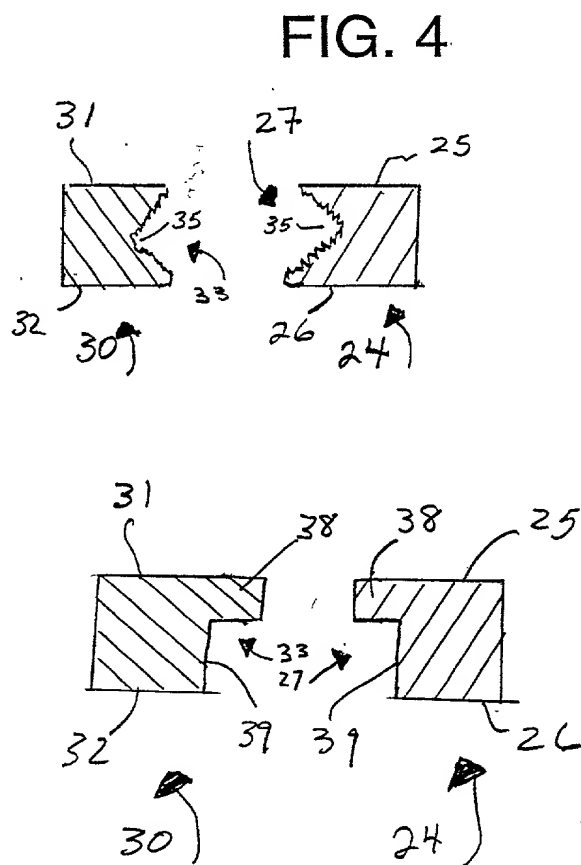
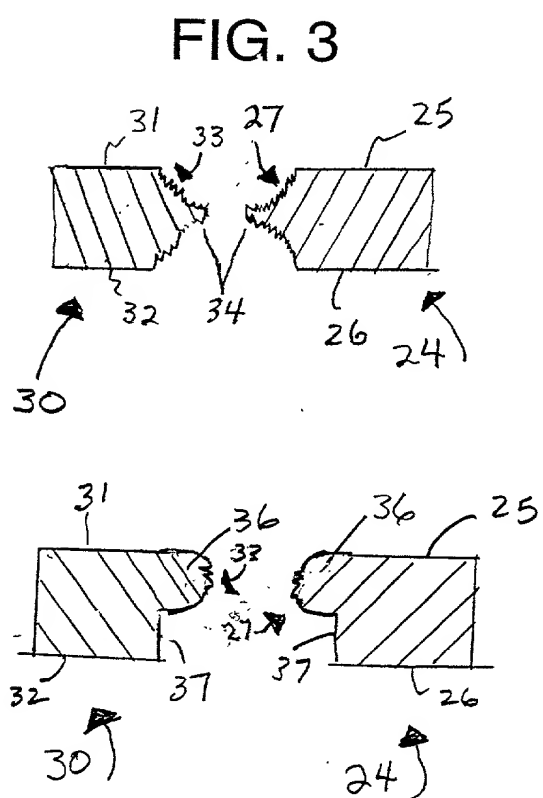
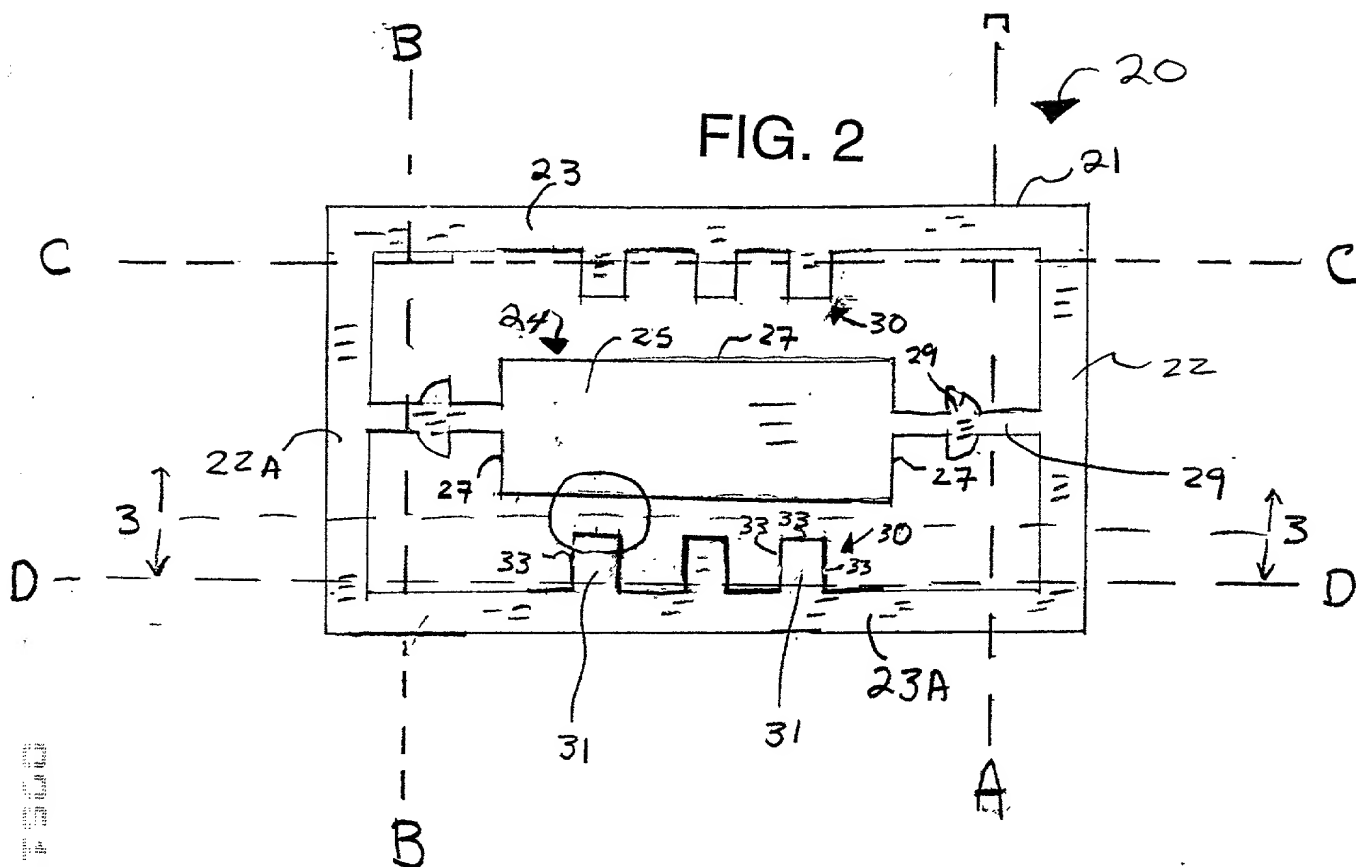
5 Packages for an integrated circuit die and methods  
and leadframes for making such packages are disclosed.  
The package includes a die, a die pad, peripheral metal  
contacts, bond wires, and an encapsulant. The die pad  
and contacts are located at a lower surface of the  
10 package. The die pad and the contacts have side  
surfaces which include reentrant portions and  
asperities to engage the encapsulant.

A method of making a package includes providing a  
metal leadframe having a die pad in a rectangular  
15 frame. Tabs extend from the frame toward the die pad.  
The die pad and tabs have side surfaces with reentrant  
portions and asperities. A die is attached to the die  
pad. The die is electrically connected to the tabs.  
An encapsulant is applied to the upper and side  
20 surfaces of the leadframe. Finally, the leadframe is  
cut in situ so that the die pad and tabs are severed  
from the frame, the sides of the package are formed,  
and the package is severed from the leadframe.

**FIGURE 1**







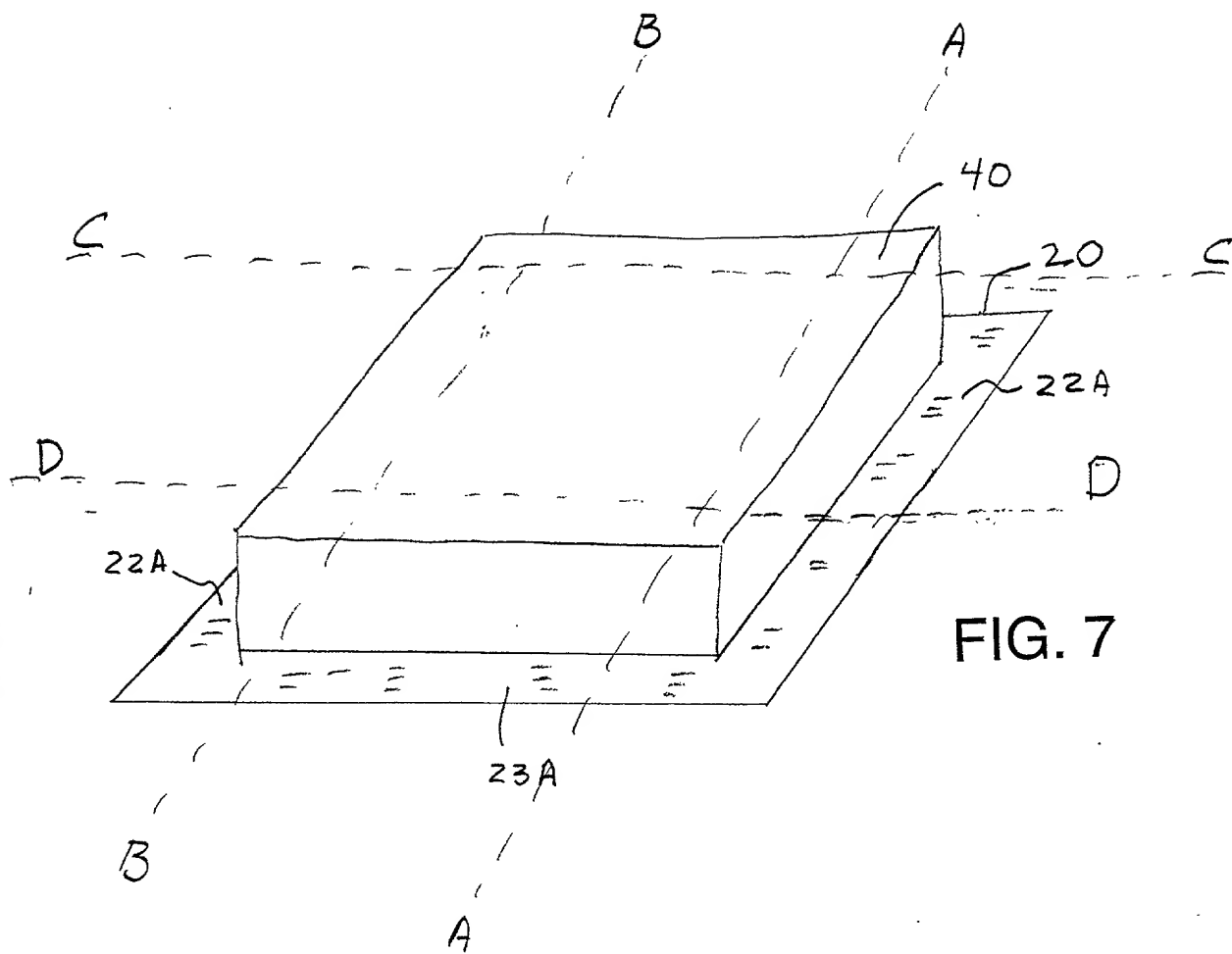


FIG. 8

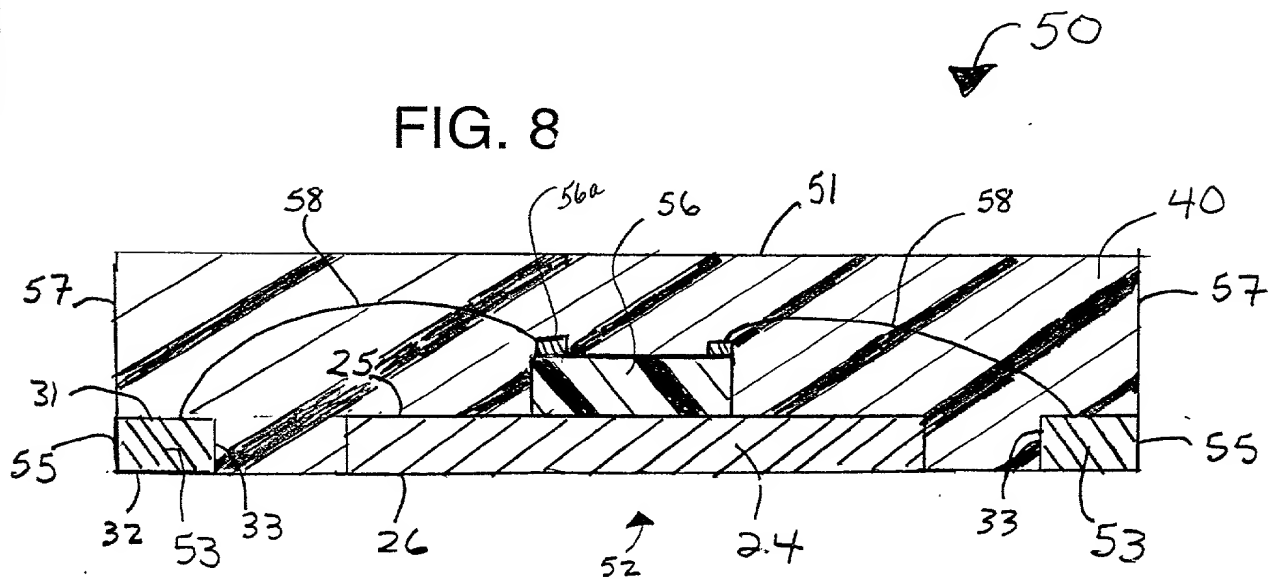


FIG. 9

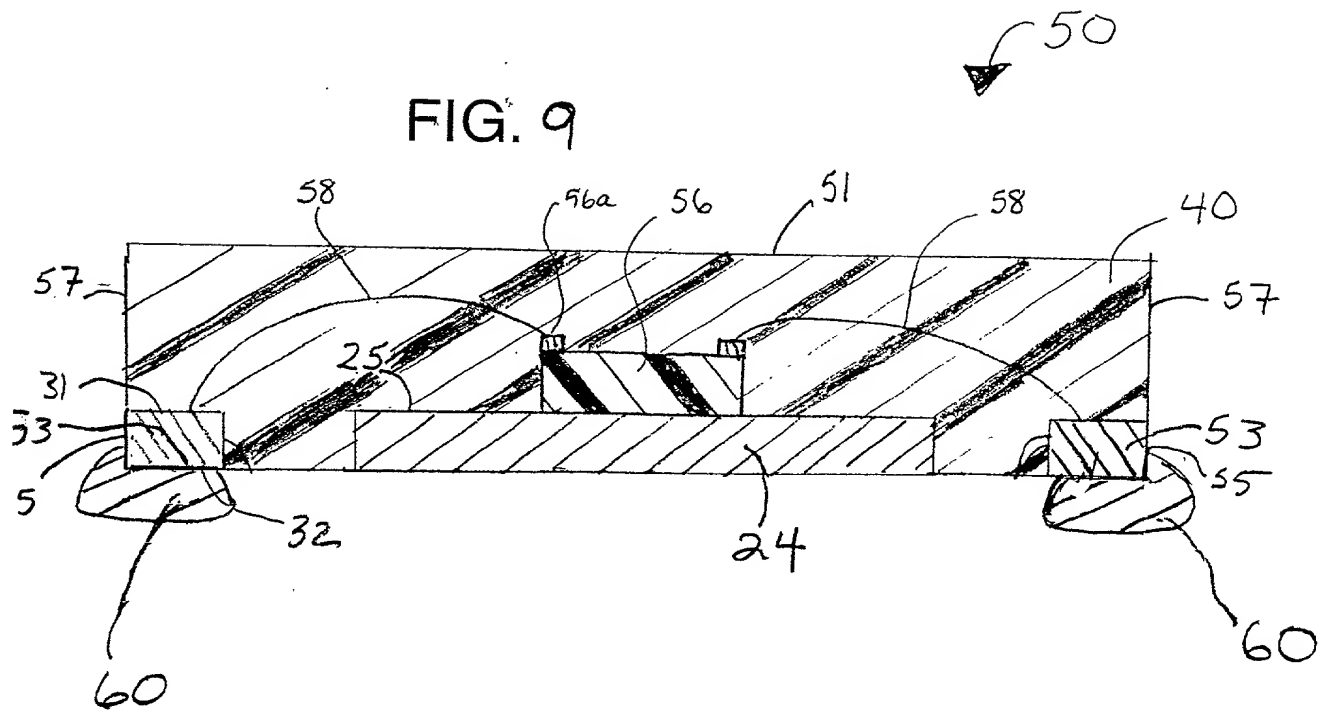
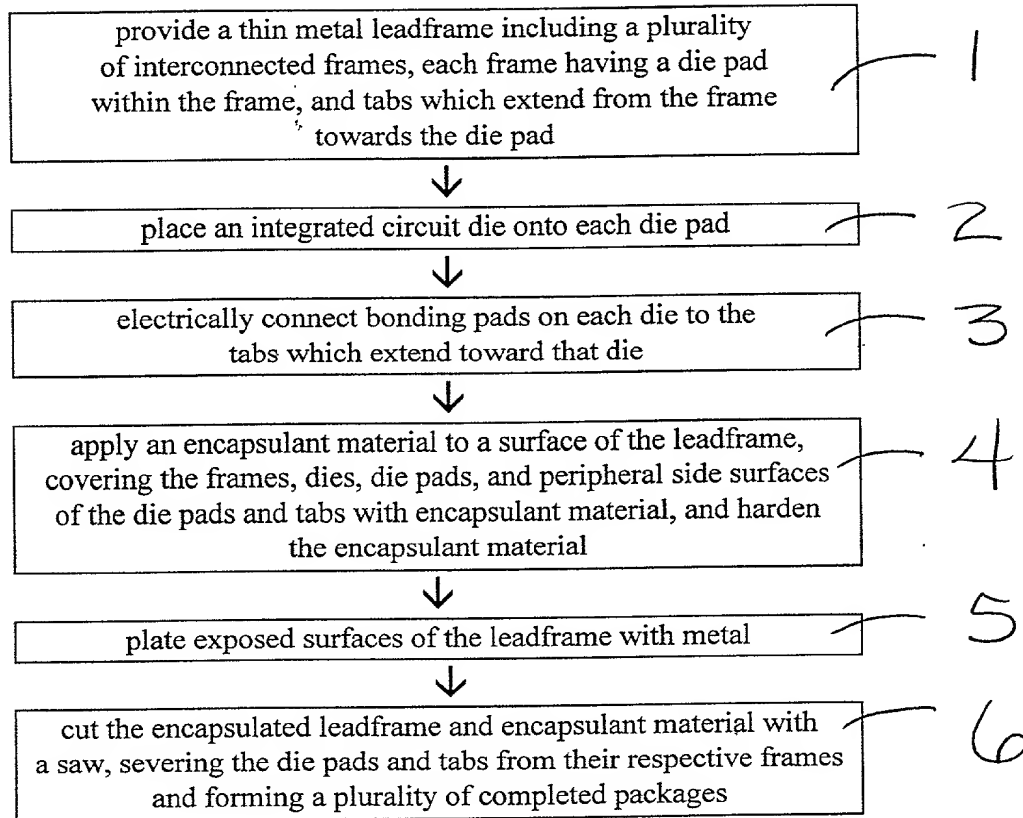


FIGURE 10



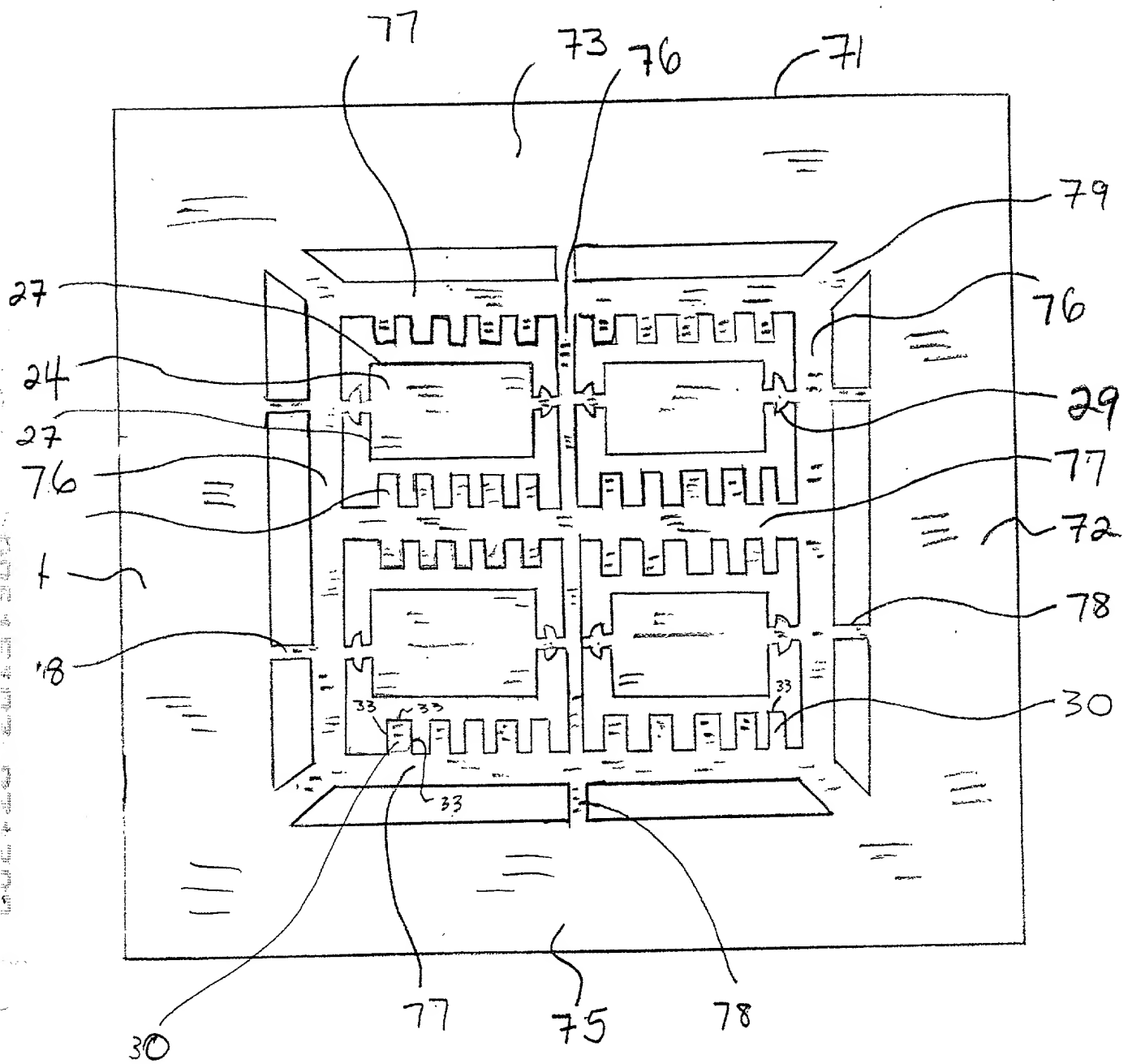


FIG. 11

## DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled Plastic Integrated Circuit Package And Method And Leadframe For Making The Package

which (check) ☒ is attached hereto.

☐ and is amended by the Preliminary Amendment attached hereto.

☐ was filed on \_\_\_\_\_ as Application Serial No. \_\_\_\_\_

☐ and was amended on \_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
Number	Country	Day/Month/Year Filed	Yes	No
N/A			<input type="checkbox"/>	<input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

Provisional Application Number	Filing Date
N/A	

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or PCT international application(s) designating the United States of America listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal

Regulations, § 1.56, which became available between the filing date of the prior application(s) and the national or PCT international filing date of this application:

Application Serial No.	Filing Date	Status (patented, pending, abandoned)
N/A		

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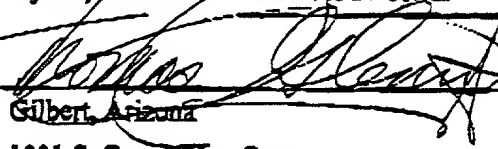
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